

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Chen et al.

Serial No.

Group Art Unit:

Filed:

Herewith

Examiner:

Title: RECONSTRUCTING MEMORY RESIDENT QUEUES OF
AN INACTIVE PROCESSOR

Assistant Commissioner for Patents
Washington, D.C. 20231

STATEMENT OF RELEVANCE FOR INFORMATION DISCLOSED BY APPLICANT

Sir:

The following Statement of Relevance is submitted with the
accompanying Form PTO-FB-A820.

Document

Designation

JP11328003

Relevance

Queuing time conformity judging circuit in
memory control system - changes time
intervals of queueing time when there is no
conformity of queueing time of access cycle
in synchronous type recording medium

A full text copies of the art cited, or the pertinent
portions thereof, is enclosed. It is respectfully requested that
this art be considered by the Examiner in the above-entitled
application and made of record therein. The information provided
and reference enclosed herewith shall not be construed as a
representation that a search has been made or that no other art
than that identified exists.

Respectfully submitted,

August 23, 2001

Date

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